



JEPPIAAR INSTITUTE OF TECHNOLOGY

(An Autonomous Institution)

Self-Belief | Self Discipline | Self Respect

Kunnam, Sunguvarchatram, Sriperumbudur-631604



DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

FACULTY DEVELOPMENT PROGRAM

PROGRAM TITLE: RECENT TRENDS IN DEMOCRATISATION OF CHIP DESIGN

DATE: 12-06-2023 to 16-06-2023

VENUE: Online

TOTAL PARTICIPANTS: 51

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BROCHURE:

JEPPIAAR INSTITUTE OF TECHNOLOGY
 "SELF BELIEF | SELF DISCIPLINE | SELF RESPECT"
 KUNNAM, SUNGUVARCHATRAM, SRIPERUMBUDUR, CHENNAI - 631604.

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING
FACULTY DEVELOPMENT PROGRAMME
 ON
RECENT TRENDS IN DEMOCRATIZATION OF CHIP DESIGN.
DATE: 12-06-2023 TO 16-06-2023

DAY 1
 TOPIC: OVERVIEW OF VLSI AND SOC CHIP
 Dr. P. KALPANA DEVI, ASSOCIATE PROFESSOR -ECE, VELTECH RANGARAJAN Dr. SAGUNTHALA RSD INSTITUTE OF SCIENCE AND TECHNOLOGY, AVADI.

DAY 2
 TOPIC: SOFTCORES FOR FPGA THE FREE AND OPEN SOURCE HARDWARE.
 Satyadhyam Chickerur Professor, Computer Science and Engineering Head, Center for High Performance Computing KLE Technological University

DAY 3
 TOPIC : A DAY WITH VLSI CIRCUITS.
 B. Samuel Jigme Harrison Semiconductor Professional (Senior Member -IEEE), Singapore

DAY 4
 TOPIC - SOC AND CHIP DESIGNING.
 Dr.S.R.MALATHI - PROFESSOR, ELECTRONICS AND COMMUNICATION ENGINEERING, SRI VENKATESWARA COLLEGE OF ENGINEERING, PENNALAR, SRIPERUMBUDUR, CHENNAI.

DAY 5
 TOPIC : SYSTEM ON CHIP.
 Radhika Rajendran - Professor Electronics and Communication Engineering S.A.Engineering College, Chennai-77

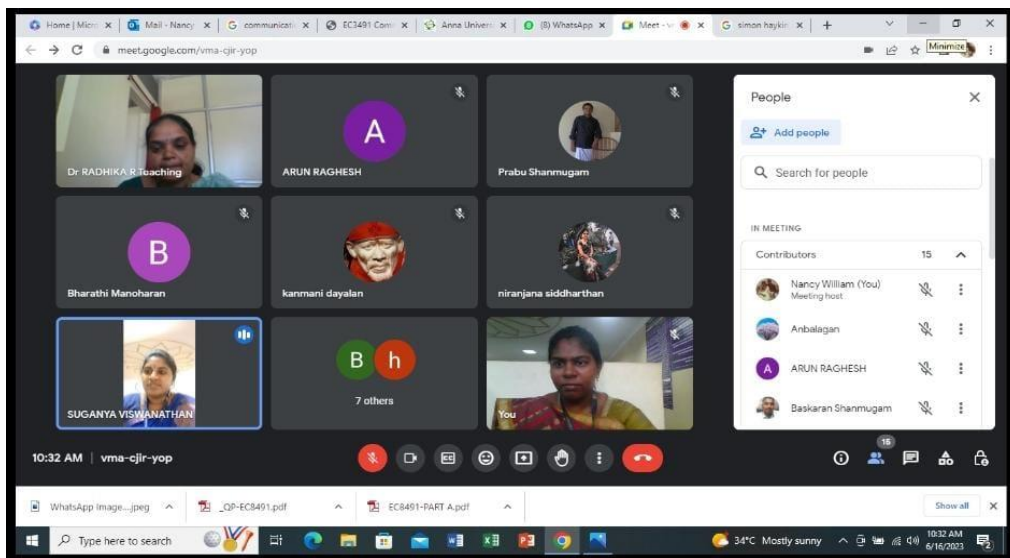
CHIEF PARTON:
 Dr. N. MARIE WILSON, MANAGING DIRECTOR, JEPPIAAR INSTITUTE OF TECHNOLOGY.

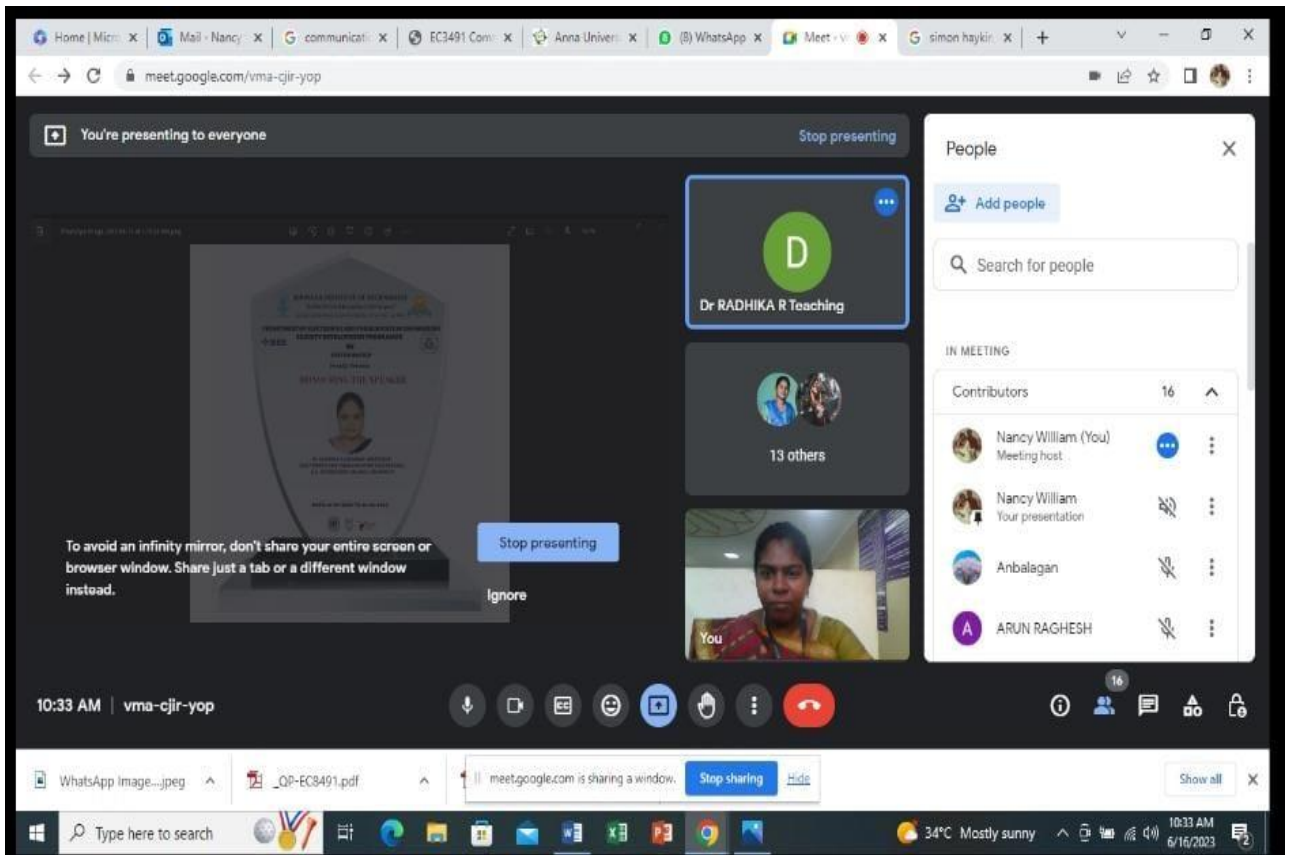
PATRON :
 Dr. L. M. MERLIN LIVINGSTON, PRINCIPAL, JEPPIAAR INSTITUTE OF TECHNOLOGY

STAFF COORDINATORS:
 Mrs. NANCY W, HOD - ECE, JEPPIAAR INSTITUTE OF TECHNOLOGY.

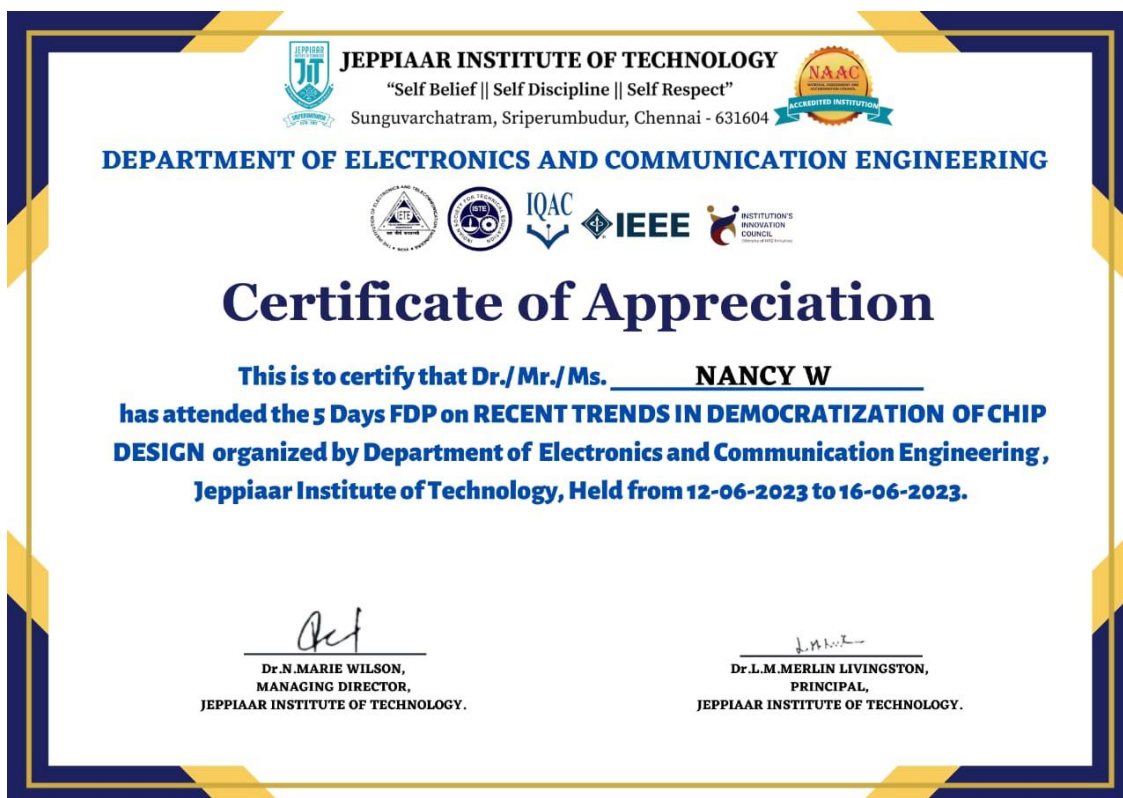
STAFF COORDINATOR:
 Mrs. INDUMATHI M, AP - ECE, JEPPIAAR INSTITUTE OF TECHNOLOGY.

PROGRAM PROOF PHOTOS:





CERTIFICATE:



FEEDBACK AND RESPONSE:

NAME:Anbazhgan

DESIGNATION/ DEPT:ASSISTANT PROFESSOR/ECE

1.The objectives of the Program were conveyed effectively

<i>1</i> <i>(Strongly disagree)</i>	<i>2</i> <i>(Disagree)</i>	<i>3</i> <i>(Neither agree nor Disagree)</i>	<i>4</i> <i>(Agree)</i>	<i>5</i> <i>(Strongly Disagree)</i>
			✓	

2.The content of the program was useful and interesting

<i>1</i> <i>(Strongly disagree)</i>	<i>2</i> <i>(Disagree)</i>	<i>3</i> <i>(Neither agree nor Disagree)</i>	<i>4</i> <i>(Agree)</i>	<i>5</i> <i>(Strongly Disagree)</i>
			✓	

3.The program was well structured and well organised

<i>1</i> <i>(Strongly disagree)</i>	<i>2</i> <i>(Disagree)</i>	<i>3</i> <i>(Neither agree nor Disagree)</i>	<i>4</i> <i>(Agree)</i>	<i>5</i> <i>(Strongly Disagree)</i>
			✓	

4.Presentation by the resource persons were easily perceptible

<i>1</i> <i>(Strongly disagree)</i>	<i>2</i> <i>(Disagree)</i>	<i>3</i> <i>(Neither agree nor Disagree)</i>	<i>4</i> <i>(Agree)</i>	<i>5</i> <i>(Strongly Disagree)</i>
			✓	

5.This program increased my knowledge and skills in Teaching Learning process

<i>1</i> <i>(Strongly disagree)</i>	<i>2</i> <i>(Disagree)</i>	<i>3</i> <i>(Neither agree nor Disagree)</i>	<i>4</i> <i>(Agree)</i>	<i>5</i> <i>(Strongly Disagree)</i>
			✓	

NAME: Arun.Raghes

DESIGNATION/ DEPT: ASSISTANT PROFESSOR/ECE

1. The objectives of the Program were conveyed effectively

<i>1</i> <i>(Strongly disagree)</i>	<i>2</i> <i>(Disagree)</i>	<i>3</i> <i>(Neither agree nor Disagree)</i>	<i>4</i> <i>(Agree)</i>	<i>5</i> <i>(Strongly Disagree)</i>
			✓	

2. The content of the program was useful and interesting

<i>1</i> <i>(Strongly disagree)</i>	<i>2</i> <i>(Disagree)</i>	<i>3</i> <i>(Neither agree nor Disagree)</i>	<i>4</i> <i>(Agree)</i>	<i>5</i> <i>(Strongly Disagree)</i>
			✓	

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			✓	

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			✓	

FACULTY DEVELOPMENT PROGRAMME

on

“RECENT TRENDS IN DEMOCRATISATION OF CHIP DESIGN”

DAY 1: OVERVIEW OF VLSI AND SOC CHIP

SPEAKER: Dr.P.Kalpana Devi

Associate Professor - Electronics and Communication Engineering Department
Vel Tech Rangarajan Dr.Sagunthala R&D Institute of Science and Technology

DAY 2: SOFTCORES FOR FPGA THE FREE AND OPEN SOURCE AND HARDWARE

SPEAKER: Satyadhyan Chickerur

Professor - Computer Science and Engineering Head,
Center for High Performance Computing KLE Technological University

DAY 3: A DAY WITH VLSI CIRCUITS

SPEAKER: B.Samuel Jigme Harrison

Semiconductor Professional (Senior Member - IEEE), Singapore

DAY 4: SOC AND CHIP DESIGNING

SPEAKER: Dr.S.R.Malathi

Professor - Electronics and Communication Engineering Department
Sri Venkateshwara College of Engineering, Pennalur, Sriperumbudur, Chennai.

DAY 5: SYSTEM ON CHIP

SPEAKER: Radhika Rajendran

Professor - Electronics and Communication Engineering Department
S. A. Engineering College, Chennai-77

OUTCOME:

Special thanks to all organizers, speakers, and participants for making this FDP a successful endeavour in promoting democratization in chip design.